

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

processor &lt;near&gt; mailbox &lt;near&gt; DMA

Search

[Advanced Scholar Search](#)[Scholar Preferences](#)[Scholar Help](#)**Scholar** All articles - [Recent articles](#) Results 1 - 10 of about 450 for **processor <near> mailbox <near> DMA**. (0.22 seconds)Did you mean: [processor <near> mailbox <near> DAM](#)[\[PDF\] • REVISION HISTORY](#)

BP Core, **DMA** Controllers, RT Clock, W Timer, SP ... - analog.com  
 ... The memory **DMA** controller provides high bandwidth data ... ory mapped registers (MMRs)  
 at addresses **near** the top of ... The ADSP-BF539/ADSP-BF539F **processor** contains a ...  
[Related articles](#) - [View as HTML](#) - [Web Search](#) - [All 37 versions](#)

[Power Efficient Processor Architecture and The Cell BE™ Processor](#) - [PDF] • [uoa.gr](#)

HP Holstee - Proceedings of the 11th International Symposium on High- ..., 2005 - doi.ieee.computersociety.org  
 ... as well as a memory mapped **mailbox** per SPE ... The SPE **processor** incorporates many of  
 the characteristics that ... for it to achieve high frequency and **near** optimal CPI ...  
[Cited by 114](#) - [Related articles](#) - [Web Search](#) - [All 17 versions](#)

[A Buffered-Mode MPI Implementation for the Cell BE™ Processor](#) - [PDF] • [isu.edu](#)

A Kumar, G Senthikumar, M Krishna, N Jayam, PK ... - Proceedings of the 7th International Conference on ... - Springer  
 ... 1 A **mailbox** message is a fast communication mechanism for ... OS support that is expected  
 in the **near** future ... significant obstacle to the use of the Cell **processor**. ...  
[Cited by 2](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)

[A digital processing system for source location and sound capture by large microphone arrays](#)

HF Silverman, WR Patterson, JL Flanagan, D ... - Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., ..., 1997 - [ieeexplore.ieee.org](#)  
 ... boxes, called microphone mod- ules, mounted **near** the array ... The DSP **processor** programs  
 have no in- fluence on ... is used as a **mailbox** for data entering or leaving ...  
[Cited by 26](#) - [Related articles](#) - [Web Search](#) - [All 3 versions](#)

[\[CITATION\] Micros Join Intelligent Networks](#)

HA RAPHAEL - Microprocessors: New Directions for Designers, Selected from ..., 1975 - Hayden Book Co Inc., US  
[Cited by 1](#) - [Related articles](#) - [Web Search](#)

[\[PDF\] • Realization of Embedded Multimedia System Based On Dual-Core Processor OMAP5910](#)

P Li, Y Lu, S Li, H Wei - International Journal of Computers, Communications & Control, 2006 - [journal.univagora.ro](#)  
 ... DSP driver would process this **Mailbox** interrupt on the ... multimedia applications in  
 the **near** future ... Instruments, OMAP5910 Dual-Core **Processor** Technical Reference ...  
[Related articles](#) - [View as HTML](#) - [Web Search](#) - [All 5 versions](#)

[\[PDF\] • PCI Based Read-out Receiver Card in the ALICE DAQ System](#)

W Carena, P Csato, E Denes, R Divia, K Schossmaier ... - interface - [thc-electronics-workshop.web.cern.ch](#)  
 ... Outgoing **Mailbox** ... transfer speed is saturated at 127 MB/s, **near** the theoretical ... in  
 co-operation with the readout software running on the front- end **processor**. ...  
[Cited by 1](#) - [Related articles](#) - [View as HTML](#) - [Web Search](#) - [All 6 versions](#)

[the software coverage plan](#)

H VERIFICATION - [ieeexplore.ieee.org](#)  
 ... Define backdoor access to **mailbox** Define ports for functions and variables ... g on the  
 target **processor**. ... This is very **near** the maximum possible speed for emulation ...  
[Web Search](#)

### A single-chip digital signal processing system

JE Brewer, LG Miller, IH Gilbert, JF Melia, D ... - Wafer Scale Integration, 1994. Proceedings., Sixth Annual ..., 1994 - IEEE Explore IEEE.org

... In the **near** future the device will ... to/from the **processor** through any of eight **mailbox** registers. ... read or write any location in the **processor's** internal memory. ...

[Related articles](#) - [Web Search](#)

### [PDF] •ALMA Correlator Real-Time Data Processor

J Pisano, R Amestica, J Perez - accelconf.web.cern.ch

... The Quick Look Pipeline provides **near** real-time imaging of ... ALMA Correlator Real-Time Data **Processor** ... Using the RTAI **mailbox** facility, the timing event kernel ...

[Related articles](#) - [View as HTML](#) - [Web Search](#) - [All 2 versions](#)

Key authors: [H Hofstee](#) - [J Kahle](#) - [C Johns](#) - [M Day](#) - [T Maeurer](#)

Did you mean to search for: [processor <near> mailbox <near> DAM](#)

Google 

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2008 Google